

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Applicant: Joy et al.	
App. No.: 10/779,944	Con. No.: 9355
Filed: February 17, 2004	Art Unit: 2154
Title: SWITCHING METHOD IN A MULTI-THREADED PROCESSOR	Examiner: Donaghue, Larry D.

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §§1.97(b)(4) and 1.98**

MAIL STOP AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

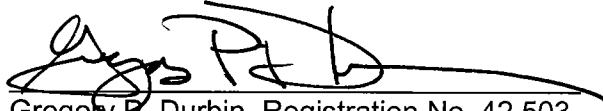
The Examiner is requested to consider the references noted on the enclosed Form PTO/SB/08a during examination of the above-identified patent application. These references are submitted for the Examiner's consideration and are submitted pursuant to the duty of disclosure under 37 C.F.R. § 1.56. In submitting these references, no representation is made or implied that the references are or are not material to the examination of the application. The Examiner is encouraged to make his or her own determination of materiality. Copies of the references are enclosed for ready review.

This Supplemental Information Disclosure Statement is being filed before the first Office action, after the filing of a Request for Continued Examination. Accordingly, pursuant to 37 C.F.R. § 1.97(b)(4), no fees are due with respect to this filing. However, should any such fees or petitions be required, please consider this a request therefor and authorization to charge Deposit Account No. 04-1415 as necessary.

If the Examiner has any questions, please contact the undersigned attorney.

Dated: July 16, 2007

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Greg P. Durbin', written over a horizontal line.

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<b>PTO/SB/08A (08-03)</b> <b>U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE</b> <b>Substitute for Form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>	<b>APPLICATION NO.:</b> 10/779,944	<b>FILING DATE:</b> February 17, 2004
	<b>INVENTOR(S):</b> Joy et al.	<b>ART UNIT:</b> 2154
	<b>EXAMINER NAME:</b> Donaghue, Larry D.	<b>ATTY. DOCKET NO.:</b> 188538/US/3

U.S. PATENT DOCUMENTS					
EXAMINER INITIALS*	Cite No. <sup>1</sup>	PATENT NUMBER	ISSUE DATE	Name of Patentee of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number – Kind Code <sup>2</sup> (if known)	MM-DD-YYYY		

FOREIGN PATENT DOCUMENTS						
EXAMINER INITIALS*	Cite No. <sup>1</sup>	DOCUMENT NUMBER	PUBLICATION DATE	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> – Number <sup>4</sup> – Kind Code <sup>5</sup> (if known)	MM-DD-YYYY			

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
EXAMINER INITIALS*	Cite No. <sup>1</sup>	Include name of Author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), volume-issue number(s), publisher, page(s) and date.	T <sup>6</sup>
	1.	Farrens et al., "Strategies for Achieving Improved Processor Throughput", ACM Sigarch, Computer Architecture, Volume 19, Number 3, pp. 362-369, May 1991.	
	2.	Bernard K. Gunther, "Multithreading with Distributed Functional Units", IEEE Transactions on Computers, Volume 46, Number 4, pp. 399-411, April 1, 1997.	
	3.	Horel et al., "UltraSPARC-III: Designing Third-Generation 64-Bit Performance", IEEE Micro, Volume 19, Number 3, pp. 73-85, May-June 1999.	
	4.	Russell et al., "Shifting Register Windows", IEEE Micro, Volume 13, Number 4, pp. 28-35, August 1993.	
EXAMINER		DATE CONSIDERED	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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